

Docket No.: P2002,0587

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

U.S. Patent and Trademark Office  
JAN 24 2005  
Applic. No. : 10/616,114  
Applicant : Thomas Hanuschek, et al.  
Filed : July 9, 2003  
Art Unit : 2818  
Title : Integrated Microcontroller Module and Method for Checking  
the Functionality of an Integrated Memory of the  
Microcontroller Module  
Docket No. : P2002,0587  
Customer No. : 24131

**STATUS LETTER**

Commissioner for Patents,  
P.O. Box 1450, Alexandria, VA 22313-1450

Sir:

It is respectfully requested that this Status Letter be returned to:

LERNER AND GREENBERG, P.A.  
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Tel: (954) 925-1100  
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with an indication as to when an action may be expected.

Respectfully submitted,

Alexia J. Vrahimis

Date: January 18, 2005

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